

### ABSTRACT OF THE DISCLOSURE

An improved method for performing cyclic redundancy check (CRC). The present invention condenses a plurality of CRC sequences into a single reduced bit count CRC equivalent. The number of bits occupied by the reduced bit count CRC equivalent is fewer than the number of bits occupied by the individual CRC sequences. Thus, the present invention reduces the number of bits required to perform the CRC operation, thereby increasing the number of bits available for transporting information data bits.